

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kamel Benaissa, et al.

Docket No:

TI-30681

Serial No:

09/994,421

Conf. No:

9408

Examiner:

Jose R. Diaz

Art Unit:

2815

Filed:

11/27/2001

For:

SEMICONDUCTOR VARACTOR WITH REDUCED PARASITIC RESISTANCE

## NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF APPEALS

Mail Stop AF **Commissioner For Patents** P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 3-18-05

Applicants hereby appeal to the Board of Appeals from the decision of the Primary Examiner mailed December 10, 2004, finally rejecting Claims 11-14 and 19-22.

The item(s) checked below are appropriate:

1.		An extension of time to respond to the final rejection
		was granted on is requested for month.
2.	<u>X</u>	A timely response to the final rejection has been filed.
3.	<u>X</u>	Fee \$500.00:
		Not required (Fee paid in prior appeal)  The Commissioner of Patents is hereby authorized to charge any fees which may be required or credit any overpayment to the deposit account of Texas Instruments incorporated, Account No. 20-0668.
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03/22/2005

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500.00 DA

Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265 (972) 917-4258

Peter K. McLarty Attorney for Appellants

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